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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/591,158	06/09/2000	Monica A. Marics	MEDO 5029 PUS	2781

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EXAMINER

LIN, WEN TAI

ART UNIT	PAPER NUMBER
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2154

11

DATE MAILED: 05/10/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/591,158

Applicant(s)

MARICS ET AL.

Examiner

Wen-Tai Lin

Art Unit

2154

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 29 October 2003.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-10, 12, 14, 15, 17-24, 26, 28, 29 and 31-35 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-10, 12, 14, 15, 17-24, 26, 28, 29 and 31-35 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 09 June 2000 is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

### DETAILED ACTION

1. Claims 1-10, 12, 14-15, 17-24, 26, 28-29 and 31-35 are presented for examination.
2. The finality of the previous office action is withdrawn because of a newly found prior art **Matsuura** [U.S. Pat. No. 6075568], which is believed to have anticipated the claimed invention.
3. The text of those sections of Title 35, USC code not included in this action can be found in the prior Office Action.

### ***Claim Rejections - 35 USC § 102***

4. Claims 1-2, 7-10, 17-24, 26, 28-29 and 31-35 are rejected under 35 U.S.C. 102(e) as being anticipated by Matsuura[U.S. Pat. No. 6075568].
5. As to claims 1 and 20, Matsuura teaches the invention as claimed including: a system for providing Internet addresses corresponding to an electronic signal to a user, the system comprising:

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- a receiver for receiving at least one electronic signal, the at least one electronic signal including one or more Internet addresses embedded therein [Abstract];
- a decoder in communication with the receiver, the decoder for extracting the one or more Internet addresses from the at least one electronic signal [5, Fig.2; col.4, lines 45-48];
- a processor in communication with the decoder [9, Fig.2], the processor for compiling a historical list of the one or more Internet addresses extracted from the at least one electronic signal, wherein the processor includes memory [10, Fig.2] for storing the historical list [10a, Fig.2] and program source information indicating the program from which each Internet address was extracted [7, Fig.2; col.6, lines 1-13]; and
- a web browser connected to the processor, the web browser for presenting the historical list of the one or more Internet addresses to the user [col.4, lines 45-50].

6. As to claim 2, Matsuura teaches that the processor is further operable to receive a signal from the user indicating a selected Internet address from the historical list and provide a connection to a web page associated with the selected Internet address [Figs. 3-4].

7. As to claims 7-8, Matsuura teaches that the system further comprising a first display [13, Fig.2] in communication with the processor [7-10, Fig.2], wherein the processor is in communication with the receiver [1-14, Fig.2], and the at least one electronic signal is displayed on the first display.

8. As to claims 9-10, Matsuura teaches that the system further comprising a second display in communication with the receiver for displaying the at least one electronic signal to the user, wherein the second display includes a television set [12-13, Fig.2; note that the three switching modes T1-T3 share a same display unit, which is an obvious option to providing separate displays].

9. As to claims 17-19, Matsuura further teaches that the at least one electronic signal includes a video signal, an audio signal or both [col.1, lines 6-26; note that Matsuura's video/audio signals and character broadcast data are in the form of electronic signal].

10. As to claim 21, Matsuura further teaches that the Internet addresses are embedded in a vertical blanking interval of the at least one electronic signal [col.1, lines 17-19].

11. As to claims 22-24, 26, 28-29 and 31-35, since the features of these claims can also be found in claims 1, 22 and 25, they are rejected for the same reasons set forth in the rejection of claims 1, 22 and 25 above.

12. Claims 3-6, 12 and 14-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuura [U.S. Pat. No. 6075568], as applied to claims 1-2, 7-10, 17-24, 26, 28-29 and 31-35 above.

13. As to claims 3 and 5-6, Matsuura teaches an integrated system including a TV receiver [e.g., 1-6, Fig.2] and a processing module [7-11 and 14, Fig.2]. Matsuura does not specifically teach that the processing module may include a personal computer or a web tablet, and the receiver include a set-top box.

However, it is well known in the art that a PC or web tablet may be equipped with a TV receiver with the latter optionally built into a set-top box. Thus, it would have been obvious to one of ordinary skill in the art that Matsuura's system may optionally be viewed as a representation of two separate physical subsystem: one providing processing/browsing capability (which takes the form of a PC or a web tablet) and the other one providing the conventionally TV receiver's tuning functionality (such as a set-top box), because an integrated system may be implemented in many different ways and utilizing the existing PC's processing power and TV set-top box is a natural and straightforward implementation of Matsuura's system concept.

14. As to claim 4, Matsuura does not specifically teach that the receiver includes a home gateway.

However it is well known that a home gateway is simply a gateway to connect to a home network wherein PC or home entertainment equipments can be interconnected.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to include the home gateway as a possible source of receiver signal, because by doing so Matsuura's system can be further expanded to include other sources of video or audio signals, where relevant URLs could be retrieved from the signals and causing relevant internet information to be displayed [see also paragraphs 53-54 for motivation].

15. As to claim 12, Matsuura teaches that the system further comprises a tuner in communication with the receiver for tuning to a selected one of the plurality of electronic signals [note that this is an inherent function of a tuner (i.e., the TV receiver)].

16. As to claims 14-15, Matsuura teaches that the historical list must be stored in non-volatile memory such as EEPROM, flash memory, and the like (all providing limited memory area). Matsuura does not specifically teach that the historical list includes Internet addresses extracted over an amount of time or an amount of items selectable by the user.

However, it is well known in the art that data stored in a temporary memory such as cache has associated time-out parameter selectable by a user.

It would have been obvious to one of ordinary skill in the art at the time the invention was made that Matsuura's historical list memory also requires a similar time-out policy, because Matsuura's historical list memory is limited in space and a proper over-written policy allows newly selected data elements to be entered into the memory. Note that other policies such as replacing least-used (RLU) or first-in-first-out (FIFO) are variations of the time-out mechanism.

17. Applicant's arguments with respect to claims 1-10,12, 14-15, 17-24, 26, 28-29 and 31-35 on 10/29/2003 have been considered but are moot in view of the new ground(s) of rejection.

18. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL. See MPEP 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

19. A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of



the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Wen-Tai Lin whose telephone number is (703)305-4875. The examiner can normally be reached on Monday-Friday (8:00-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Follansbee can be reached on (703) 305-8498. The fax phone numbers for the organization where this application or proceeding is assigned are as follows:

(703)872-9306 for official communications; and

(703)746-5516 for status inquires draft communication.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)305-3900.

Wen-Tai Lin

May 5, 2004

*Wen-Tai Lin*  
*5/5/04*